

MINISTRY OF SUPPLY (R.R.E.)VALVE ELECTRONIC CV5110

Specification MOS/CV5110 Issue 1 dated 12.12.57 To be read in conjunction with K1001	<u>SECURITY</u>	
	<u>Specification</u>	<u>Valve</u>
	Unclassified	Unclassified

<u>TYPE OF VALVE:-</u> H.F. Pentode, variable- μ .				<u>MARKING</u>		
<u>CATHODE:-</u> Indirectly heated.				See K1001/4.		
<u>ENVELOPE:-</u> Glass-metallised				<u>BASE</u>		
<u>PROTOTYPE:-</u> CV1053				I.O.		
<u>RATING</u>				<u>CONNECTIONS</u>		
				Note		
Heater Voltage	(V)	6.3		Pin	Electrode	
Heater Current	(A)	0.2		1	Metallising	
Max. Anode Voltage	(V)	300		2	Heater	
Max. Screen Voltage	(V)	300		3	Anode	
Max. Anode Dissipation	(W)	2.0		4	Screen Grid	
Max. Screen Dissipation	(W)	0.3		5	Suppressor Grid	
Mutual Conductance				6	Pin Omitted.	
(1)	(mA/V)	2.2	A	7	Heater	
(2)	(mA/V)	0.0045	B	8	Cathode	
Anode Impedance				T.C.	Control Grid	
(1)	(m)	1.25	A			
(2)	(m)	10	B			
Max. Operating Frequency	(Mc/s)	20				
<u>CAPACITANCES</u> (pF)				<u>TOP CAP</u>		
C _{ae}		7.9		See K1001/AI/D5.2		
C _{ge}		5.6		<u>DIMENSIONS</u>		
C _{ag} (max.)		0.003		Dimension	Min.	Max.
<u>NOTES</u>				Overall length		
				(mm)	95	100
A. At $V_a = 250V$, $V_{g2} = 100V$. $V_{g1} = -2.5V$, $I_a = 6 mA$.				Diameter (mm)	-	32
				B. At $V_a = V_{g2} = 250V$, $V_{g1} = -49V$		

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TESTS

CV5110

To be performed in addition to those applicable in K.1001

Test Conditions						Test	Limits		No. Tested
							Min.	Max.	
See K1001/AIII						Capacitances (pF)	7.0	9.4	6 per week
Links to H.P.	Links to L.P.	Links to E			C _{ae}				
3	1, 2, 4, 5, 7, 8.	TC1, 6, 9, 10, TC2			C _{ge}				
TC1	1, 2, 4, 5, 7, 8.	3, 6, 9, 10, TC2			C _{ag}				
3	TC1	1, 2, 4, 5, 6, 7, 8, 9, 10, TC2.				-	0.003	T.A.	
	V _h (V)	V _a (V)	V _{g2} (V)	V _{g3} (V)	I _a (mA)	I _h (A)	0.18	0.22	100% or S
b	6.3	0	0	0	0				
c	6.3	250	100	0	6	V _{g1} (V)	-1.9	-3.1	100%
d	6.3	250	100	0	6	I _{g2} (mA)	1.4	2.0	100% or S
e	6.3	250	100	0	6	G _m (mA/V)	1.8	2.6	100%
f	6.3	250	100	0	6	Reverse I _g (uA)	-	0.5	100%
g	6.3	200	200	0	10uA	V _{g1} (V)	-35	-52.5	100%
h	6.3					<u>Measured Gain</u> <u>Ideal Linear Gain.</u> (See Note 2)	0.83	1.01	100%

NOTES

1. Peak grid swing \pm 0.5 volts.
2. To be rested in an equipment approved by R.R.E. See drawings page 3.

Principle of Test. This is illustrated in fig. 2 on page 3. With V_g (D.C.), (grid to earth) set to 108 volts, the ratio $\frac{\text{Measured Gain}}{\text{Ideal Linear Gain}}$ is set to be unity.

Then with V_g (D.C.) set to 48 volts the ratio $\frac{\text{Measured Gain}}{\text{Ideal Linear Gain}}$ (Y/X) shall be within the limits given.

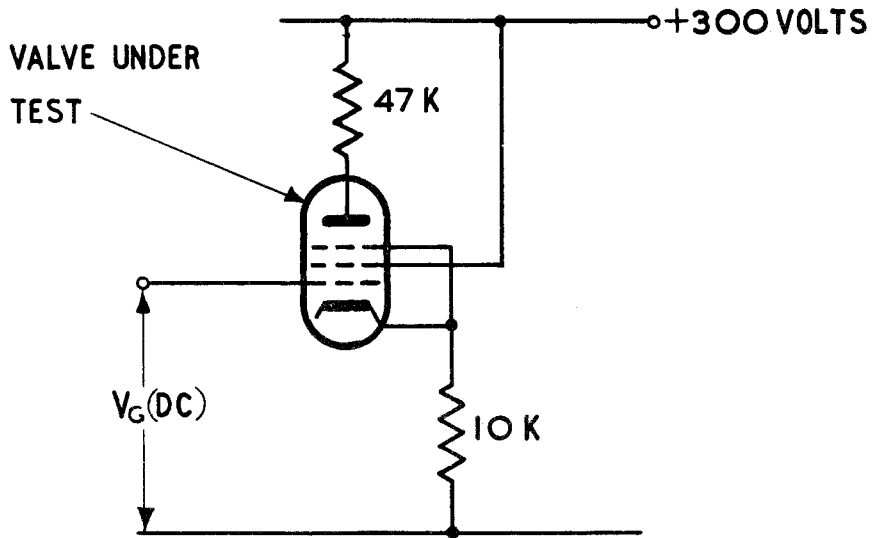


FIG. 1.
TEST CIRCUIT

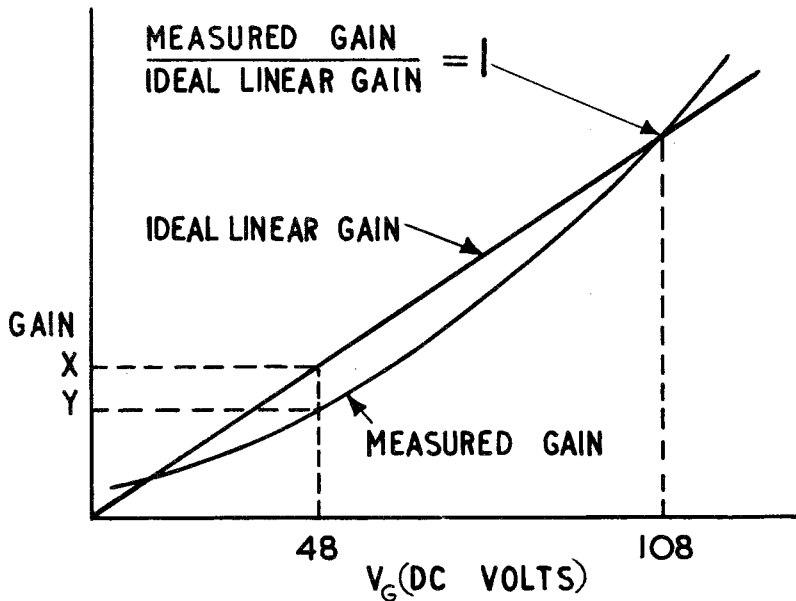


FIG. 2.
PRINCIPLE OF TEST